

DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATED MEMORY MAPPED CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVER, the specification of which

(check one) _____ is attached hereto.

XX was filed on February 5, 2001
Application Serial No. 09/777,917
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
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_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	_____ Yes	_____ No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	_____ Yes	_____ No

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulation, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) patented, pending, abandoned)

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Address all correspondence to:

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Palo Alto, California 94304

Matter/File Number: 9775-0052-999

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of **sole or first** inventor:

LEWIS B. ARONSON

Inventor's signature:

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STEPHEN G. HOSKING

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Date:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:
ARONSON et al.

Serial No.: 09/777,917

Filed: February 5, 2001

For: *Integrated Memory Mapped
Controller Circuit for Fiber
Optics Transceiver*

Confirmation No.: 6191

Art Unit: 2633

Examiner: Not yet assigned

Attorney Docket No: 9775-0052-999

REVOCATION AND POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R:

Finisar Corporation, owner of the entire right, title and interest in, to and under the invention described and claimed in the above-identified patent application hereby revokes all previous powers of attorney and appoints Berj A. Terzian (Reg. No. 20060), David Weild, III (Reg. No. 21094), Barry D. Rein (Reg. No. 22411), Philip T. Shannon (Reg. No. 24278), Francis E. Morris (Reg. No. 24615), Gidon D. Stern (Reg. No. 27469), John J. Lauter, Jr. (Reg. No. 27814), Brian M. Poissant (Reg. No. 28462), Rory J. Radding (Reg. No. 28749), Stephen J. Harbulak (Reg. No. 29166), Donald J. Goodell (Reg. No. 19766), Thomas E. Friebe (Reg. No. 29258), Laura A. Coruzzi (Reg. No. 30742), Geraldine F. Baldwin (Reg. No. 31232), Victor N. Balancia (Reg. No. 31231), Samuel B. Abrams (Reg. No. 30605), Adriane M. Antler (Reg. No. 32605), Thomas G. Rowan (Reg. No. 34419), James G. Markey (Reg. No. 31636), Thomas D. Kohler (Reg. No. 32797), Scott D. Stimpson (Reg. No. 33607), Gary S. Williams (Reg. No. 31066), Ann L. Gisolfi (Reg. No. 31956), Kelly D. Talcott (Reg. No. 39582), Francis D. Cerrito (Reg. No. 38100), Anthony M. Insogna (Reg. No. 35203), Brian M. Rothery (Reg. No. 35340), Brian D. Siff (Reg. No. 35679), Michael J. Lyons (Reg. No. 37386), Nikolaos C. George (Reg. No. 39201), Stephen S. Rabinowitz (Reg. No. 40286), Ognjan V. Shentov (Reg. No. 38051), Kenneth L. Stein (Reg. No. 38704), Andrew J. Gray (Reg. No. 41796), Henry C. Lebowitz (Reg. No. 36196), Leo Merken (Reg. No. 41192), Margaret B. Brivanlou (Reg. No. 40922), David R. Owens (Reg. No. 40756), Matthew E.

Langer (Reg. No. 36343), Karen G. Horowitz (Reg. No. 35199), T. Christopher Tsang (Reg. No. 40258), and Carl P. Bretscher (Reg. No. 41635), all of Pennie & Edmonds LLP, whose addresses are 1155 Avenue of the Americas, New York, New York 10036, 1667 K Street N.W., Washington, DC 20006 and 3300 Hillview Avenue, Palo Alto, CA 94304, and each of them, its attorneys, to prosecute this application, and to transact all business in the Patent and Trademark Office connected therewith.

Please direct all future correspondence to Gary S. Williams, Pennie & Edmonds LLP, located at 3300 Hillview Avenue, Palo Alto, California 94304, and direct all telephone calls to Pennie & Edmonds LLP at (650) 493-4935.

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Date: 7/14/03

ASSIGNEE: Finisar Corporation

Signature:



Typed Name: Frank H. Levinson

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